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[High performance integrated **processor** architecture including a sub-bus **control unit** for generating ...](#)

DD Gephardt, DS Mudgett, JR MacDonald - US Patent 5,557,757, 1996 - Google Patents
 ... **processor** includes a CPU **core**, a local bus coupled to the CPU **core**, and a ... The integrated **processor** further includes a sub-bus bus **control unit** coupled to ...
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[A 1 V heterogeneous reconfigurable **processor** IC for basebandwireless applications](#)

H Zhang, V Prabhu, V George, M Wan, M Benes, A ... - 2000 IEEE International Solid-State Circuits Conference, ..., 2000 - [ieeexplore.ieee.org](#)
 ... The Maia **processor** (Figure 4.1.3) combines an ARM8 **core** with 21 ... and an embedded low-energy FPGA [3]. Through an interface **control unit**, ARM8 config ...
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[\[PDF\] ► Designing the Low-Power M•**CORE**™ Architecture](#)

J Scott, LH Lee, J Arends, B Moyer - Power Driven Microarchitecture Workshop, 1998 - Citeseer
 ... indicate that the code density of the M•**CORE** microRISC engine is ... Fixed-length instructions also serve to reduce the CPU's **control unit** complexity, since ...
[Cited by 62](#) - [Related articles](#) - [View as HTML](#) - [Web Search](#) - [All 12 versions](#)

[Apparatus and method for guaranteed data store in redundant controllers of a process control system](#)

PF McLaughlin, PH Mody - US Patent 5,088,021, 1992 - Google Patents
 ... 3. The **processing unit** of claim 2 wherein said pro- type 68824. The receiver transmitter unit 331 can be a cessing unit is a **process control unit** and said ...
[Cited by 18](#) - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

[Central **processing unit** employing microprogrammable control for use in a data processing system](#)

RH Gruner - US Patent 3,990,052, 1976 - Google Patents
 ... 2, 1976 [54] CENTRAL **PROCESSING UNIT** EMPLOYING MICROPROGRAMMABLE CONTROL FOR USE IN A ... Jacob Frank; Joel Wall [57] ABSTRACT A microprogrammed **processor** having a ...
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[Configuration and **control unit** for a heterogeneous multi-system](#)

JL Fox, EE Marquardt - US Patent 4,075,693, 1978 - Google Patents
 ... Thus, the invention is able to distributed configuration control registers with dupli- cross-couple any I/O **control unit** to any **processor** to cated ...
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[STARAN parallel **processor** system hardware](#)

KE Batcher - Proceedings of the May 6-10, 1974, national computer ..., 1974 - [portal.acm.org](#)
 ... The PIO **control unit** controls the PIO flip network and the ... STARAN Parallel **Processor** System Hardware 499 ... control memory also has a 16K-word **core** memory with a ...
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... system providing interfaces to central **processing unit** and modular **processor**-controllers for an ...

DA Millers... - US Patent 4,106,092, 1978 - Google Patents

... INTERFACES TO CENTRAL **PROCESSING UNIT** AND MODULAR **PROCESSOR**-CONTROLLERS FOR AN INPUT-OUTPUT SUBSYSTEM [75] Inventor: Donald Alexander Millers, II., San Clemente ...

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The design and use of simplepower: a cycle-accurate energy estimation tool- ►psu.edu pdf
W Ye, N Vijaykrishnan, M Kandemir, MJ Irwin - Proceedings of the 37th conference on Design automation, 2000 - portal.acm.org

... does not capture the energy consumed by the **control unit** of the **processor** and the ...

2. The major components of SimplePower are: SimplePower **core** , RTL power ...

Cited by 303 - Related articles - Web Search - BL Direct - All 26 versions

Secure cryptographic **processor** arrangement

JT Marino Jr, RS **Core** - US Patent 4,987,595, 1991 - Google Patents

... Jr., Fountain Hills; Ronald S. **Core**, Glendale, both ... 8. A secure **processor** arrangement as claimed ... subsystem further including bus **control unit** means providing ...

Cited by 12 - Related articles - Web Search - All 5 versions

Process control system with improved system security features

DK Richer - US Patent 4,351,023, 1982 - Google Patents

... the health of the on-**control unit** before deciding ... of providing direct digital control of the **process**. ... may include a central **processing unit** (CPU), memory, and ...

Cited by 34 - Related articles - Web Search - All 2 versions

... efficiently executing a large repertoire of instructions for a high performance data **processing unit**

JE Wilhite - US Patent 4,179,736, 1979 - Google Patents

... COMPUTER **CONTROL UNIT** CAPABLE OF EFFICIENTLY EXECUTING A LARGE REPERTOIRE OF INSTRUCTIONS FOR A HIGH PERFORMANCE DATA **PROCESSING UNIT** [75] Inventor: John E ...

Cited by 18 - Related articles - Web Search - All 3 versions

A video DSP with a macroblock-level-pipeline and a SIMD typevector-pipeline architecture for MPEG2 ...

M Toyokura, H Kodama, E Miyagoshi, K Okamoto, M ... - IEEE Journal of Solid-State Circuits, 1994 - ieeexplore.ieee.org

... scheme consisting of a macroblock- level (MB: the basic **processing unit** in MPEG2 ...

The VLCNLD **processor** consists of one pro- grammable **control unit** (VPCU) and ...

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Intelligent input-output interface **control unit** for input-output subsystem

DJ Cook, DA Millers... - US Patent 4,162,520, 1979 - Google Patents

... [57] ABSTRACT An Input-Output Interface Data-Transfer **Control Unit**, designated as a Line Control **Processor**, which provides the execution of instructions in ...

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Process control for electrostatographic machine

BH Mills - US Patent 4,779,106, 1988 - Google Patents

... 2, LCU 52 consists of tempo- rary data storage memory 54, a central **processing unit** (CPU) 56, a timing and cycle **control unit** 58, a stored program control 60 ...

Cited by 21 - Related articles - Web Search - All 7 versions

Data packet switch using a primary **processing unit** to designate one of a plurality of data

[stream ...](#)

JN Chiappa - US Patent 5,249,292, 1993 - Google Patents

... specialized hardware 10 cannot **process** are passed to a software controlled primary

processing unit 11, includ- ing a central **processing unit**, CPU, 12, running ...

[Cited by 24](#) - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

[Universal process control device and method for developing a process control loop program](#)

DJ Jones, WA Edblad, JS Pringle, DM Oravetz, JR ... - US Patent 4,663,704, 1987 - Google Patents

... a general description of the distributed **processing unit** or drop ... ity of a distributed

processing **control unit** or drop ... and continu- 25 ous **process** control and ...

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[CPU clock control unit](#)

PM Bland, RT Jackson, J Joshi, J Kardach - US Patent 5,546,568, 1996 - Google Patents

... 5,546,568 CPU CLOCK **CONTROL UNIT** BACKGROUND OF ... relates to a power management

architecture scheme for a central **processing unit** ("CPU") implemented within ...

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[Multi-processor system with programmable memory-access priority control](#)

S Suzuki, S Eguchi - US Patent 4,400,771, 1983 - Google Patents

... the hardware in the 45 memory **control unit** 23 may ... 2. The multi-**processor** system includes

a plurality ... processors are the first central **processing unit** (CPU#1) 51 ...

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[\(k\)-Instructions-at-a-time pipelined processor for parallel execution of inherently sequential ...](#)

HT Hao, H Ling, HE Sachar, J Weiss, YJ Yamour - US Patent 4,594,655, 1986 - Google Patents

... INSTRUCTIONS-AT-A-TIME PIPELINED **PROCESSOR** FOR PARALLEL ... AND INTERLOCK OET.I
RESOLUTION

CONTROL UNIT Dato Flow ... Irwin, "A Pipelined **Processing Unit** for On-Line ...

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Key authors: [J Rabaey](#) - [H Zhang](#) - [D Kuck](#) - [J Scott](#) - [K Batcher](#)

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Rugged modular portable computer including modules hinged along an edge

SW Smith - US Patent 5,430,607, 1995 - Google Patents

... The **inner** filter is preferably made from a 3M (S ... The gasket 608 also provides **EMI** may be affixed to a ... 4 and **shielding** as well as moisture sealing and is soft ...

[Cited by 29](#) - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

Height reducible **EMI** shielded electronic enclosure

DP Bell, ED Burke, PE Case, DE Johnston, RL McCord ... - US Patent 6,111,192, 2000 - Google Patents

... frame covered by an appropriate **inner** and outer ... by an externally mounted environmental **control unit** (ECU) 126 ... claim 2 wherein the gasket provides **EMI shielding**. ...

[Cited by 4](#) - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

Composite Unit Accelerated Life Test of the High Gain Array

SL Arnett, AV Bray, SC Liberty - 1993 - Storming Media

... 10 3.2 **EMI Shield** Evaluation ... potted and connected to a stave cable consisting of 30 bundled conductors, an outer braided binder, and an **inner** strength member ...

[Web Search](#)

Data current coupler and methods of making and assembling same

WJ Rudy Jr, HR Shafter - US Patent 4,904,879, 1990 - Google Patents

... 194,196 to receive Cover 220 includes pivot pins 28 (in phantom) and also **inner** legs 186 ... For best **EMI/RFI** protection, all seams between **shield** member 200 ...

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The death of smart sensors and effectors for future combat aircraft

RA Edwards, B Gleaves - Microprocessors and Microsystems, 1997 - Elsevier

... the environment and rough handling, as well as **EMI shielding**. ... The **core** network is likely to be based ... allows all control processes (including **inner** loop closure ...

[Web Search](#) - [All 3 versions](#)

Antenna tuning control circuit

ND Terk, JA Lofgren, WR Steinike - US Patent 5,812,066, 1998 - Google Patents

... control signal generated by a user-operated remote **control unit**, the signal ... metal housing substantially surrounds the pro-cessor for **shielding** the dipole arms ...

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Antenna tuning control system

ND Terk, JA Lofgren, WR Steinike - US Patent 6,016,108, 2000 - Google Patents

... for sensing a wireless control signal generated by a remote **control unit** and (b ... metal housing substantially surrounds the pro-cessor for **shielding** the dipole ...

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... a complete computer system including a sub-printed wiring board attached to an **inner** surface of the ...

K Tsukada, N Nakamura, M Nimura, H Suemori, T ... - US Patent 5,710,693, 1998 - Google Patents

... HAVING A FLAT, CARD-LIKE CASING ENCLOSING COMPONENTS FOR A COMPLETE COMPUTER SYSTEM

INCLUDING A SUB- PRINTED WIRING BOARD ATTACHED TO AN **INNER** SURFACE OF THE ...

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Mounting system for current mode coupler

WJ Rudy Jr, HR Shaffer, DE Stahl - US Patent 5,435,512, 1995 - Google Patents

... **control unit**, with the **control unit** providing electrical ... Electromagnetic **shielding** by using metallic plat- ing on the housing provides **EMI**/RFI protection. ...

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PUSHBUTTON CHANNEL SELECTING AND VOLTAGE DIVIDER PRESETTING UNIT

US Patent 3,522,542, 1970 - Google Patents

... ETAL 3,522,542 PUSHBUTTON CHANNEL SELECTING AND VOLTAGE **EMI** j e ... so that they can act on the **inner** TMth /ald ... for a switching unit of a remote **control unit**; 'FIG ...

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System and methods for electronic control of an accumulator fuel system

SA Thompson, J Daiker, JA Stavnhelm, W Meyer, G ... - US Patent 5,678,521, 1997 - Google Patents

Page 1. US005678521A United States Patent [igj [ii] Patent Number: 5,678,521

[45] Date of Patent: Oct. 21, 1997 Thompson et al. [54 ...

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[PDF] ► Optical Closed-Loop Propulsion Control System Development

GL Poppel, Lewis Research Center - 1998 - ntrs.nasa.gov

... for the F404 ECU (electrical **control unit**) to be ... technology will provide immunity to **EMI** (electromagnetic interference ... micron **core** fiber optic pins per MIL-T ...

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Paul Cook: Pioneer in Heat-Shrinkable Insulation

EIM Exclusive - IEEE Electrical Insulation Magazine, 1989 - ieeeexplore.ieee.org

... many new products based on variations of this **core** technology—for instance, dual-wall tubings that create a moisture-proof seal, **shielded** molded parts that ...

[Web Search](#)

Electronic device having connector with rows of terminals divided by ribs and ground or power

...
K Tsukada, N Nakamura, M Nimura, H Suemori, T ... - US Patent 5,481,432, 1996 - Google Patents

Page 1. US005481432A United States Patent Tsukada et al. [ii] Patent Number:

[45] Date of Patent: 5,481,432 Jan. 2, 1996 [54] ELECTRONIC ...

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Electric arc power collection for high-speed trains

D Klapas, R Hackam, FA Beanson - Proceedings of the IEEE, 1976 - ieeeexplore.ieee.org

... The available power on the vehicle may be doubled by add- ing an iron **shield** under the ... ductive coupling is a variation of the “open **core**” transformer [20]. ...

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Electronic device having a flat, card-like casing enclosing components for a complete computer ...

K Tsukada, N Nakamura, M Nimura, H Suemori, T ... - US Patent 5,502,617, 1996 - Google Patents

Page 1. US005502617A United States Patent Tsukada et al. [ii] Patent Number:

5,502,617 [45] Date of Patent: *Mar. 26, 1996 [54] ELECTRONIC ...

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EMP Design and Test Guidelines for Systems in Mobile Shelters

[SC Sanders](#) - 1993 - [Storming Media](#)

... 34 5.5 **Shield** Density Currents 34 ...

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Display apparatus providing a uniform temperature distribution over the display unit

[T Shimada](#), [K Kurematsu](#), [H Takabayashi](#), [S Yabu](#), [M ...](#) - 1996 - [freepatentsonline.com](#)

... diagram of another lighting device power supply **control unit**. ... a structure of an electromagnetic **shielding** plate disposed on an **inner** surface of a rear ...

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Common receive module for a programmable digital radio

[WC Phillips](#), [MV Pascale](#), [RW Minarik](#), [KM Schmidt](#), ... - US Patent 5,859,878, 1999 - [Google Patents](#)

... The digital signals are supplied to a programmable signal **processing unit** which is configured, according to the selected radio application and, if present, the ...

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Fluorescent lamp ballast having a resonant output stage using a split resonating inductor

[MS Moisin](#) - US Patent 5,982,111, 1999 - [Google Patents](#)

... or Firm—Knobbe, Martens, Olson & Bear, LLP [57] ABSTRACT An improved ballast circuit for use with a compact fluo- res- cent lamp includes an **EMI** filter, a ...

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Key authors: [S Smith](#) - [K Tsukada](#) - [W Rudy](#) - [S Thompson](#) - [N Nakamura](#)

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Fang: A firewall analysis engine- [princeton.edu](#) [PDF]

A Mayer, A Wool, E Ziskind - 2000 IEEE Symposium on Security and Privacy, 2000. S&P 2000. ..., 2000 - [ieeexplore.ieee.org](#)

... providing the rest. Behind the DMZ is the **internal firewall**, which guards the corporation's intranet. This **firewall** has three ...

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Network firewalls- [psu.edu](#) [PDF]

SM Bellovin, WR Cheswick - IEEE Communications Magazine, 1994 - [ieeexplore.ieee.org](#)

... the outside gateway has to other **internal** hosts. Either filter, or for that matter the gateway itself, may be omitted; the details will vary from **firewall** to **firewall**. ...

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[PDF] **The inevitability of failure: The flawed assumption of security in modern computing environments**

PA Loscocco, SD Smalley, PA Muckelbauer, RC Taylor ... - Proceedings of the 21st National Information Systems ..., 1998 - [cs.unomaha.edu](#)

... Modern **firewall** architectures typically involve the use of bastion hosts; in a ... to outsiders, and an internal bastion host on the **internal** network, which is ...

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Distributed firewalls- [kernel.org](#) [PDF]

SM Bellovin - Journal of Login, 1999 - [usenix.org](#)

... external access. Just as file permissions enforce an **internal** security policy, a **firewall** can enforce an external security policy. ...

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Designing an academic firewall: policy, practice, and experience with SURF- [ncstrl.org](#) [PDF]

M Greenwald, SK Singhal, JR Stone, DR Cheriton - Network and Distributed System Security, 1996., Proceedings ..., 1996 - [ieeexplore.ieee.org](#)

... The proxies run on a bastion host, an **internal** host configured specifically for the purpose. Unlike many traditional designs, our **firewall** permits multiple ...

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[PDF] **Evaluating and improving firewalls for IP-telephony environments**

U Roedig, R Ackermann, R Steinmetz ... - The 1st IP Telephony Workshop, Berlin, 2000 - [securitytechnet.com](#)

... As we have shown, a commonly used **internal firewall** architecture as shown in Figure 2 is not very useful for IP-Telephony scenarios. ...

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Security in mobile agent system: problems and approaches- [essex.ac.uk](#) [PDF]

Y Kun, G Xin, L Dayou - ACM SIGOPS Operating Systems Review, 2000 - [portal.acm.org](#)

... **Firewall** would hopefully not hinder **internal** people trying to get their job done, yet would restrict external malcontents. While ...

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Multilayer **firewall** system

DM Nessett, WP Sherer - US Patent 5,968,176, 1999 - Google Patents

... This approach has value, but introduces bottlenecks to the corporation intranet.

That is **internal firewalls** adversely affect performance within the company. ...

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[PDF] ► A distributed **firewall** for multimedia applications

U Roedig, R Ackermann, C Rensing, R Steinmetz - Proceedings of the Workshop "Sicherheit in Mediendaten, 2000 - cs.ucc.ie

... To fulfil the first criterion we specify requirements for the **internal firewall** communication subsystem as follows: - The **firewall** components, eg filters or ...

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Firewalls for security in wireless networks

U Murthy, O Bukhres, W Winn, E Vanderdez, C Inc, ... - System Sciences, 1998., Proceedings of the Thirty-First ..., 1998 - [ieeexplore.ieee.org](#)

... perhaps the most signi cant se- curity bene ts of a **Firewall** [Ranum92 ... The bastion host providesthe link between the screen- ing router and **internal** router and ...

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Putting it together a multi-dimensional approach to Internet security

FM Avolio - NetWorker, 1998 - [portal.acm.org](#)

... An organiza- tion may deploy additional **internal firewalls** — to protect the accounting department, for example, from the rest of the organization as well as ...

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Firewall system for protecting network elements connected to a public network

CD Coley, RE Wesinger Jr - US Patent 5,826,014, 1998 - Google Patents

... The connection 212 between the **internal** network 214 and the **firewall** 210 is preferably the only connection between these two elements. ...

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[CITATION] Implementing a Distributed **Firewall**

SM Bellovin, JM Smith - ACM Conference on Computer and Communications Security..., 2000 - ACM

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Information security and the Internet

GR Doddrell - Internet research: Electronic networking applications and ..., 1996 - [emeraldinsight.com](#)

... **Internal/ external router Firewall** boundary on user site Figure 5 Best practice **firewall** configuration – combined **internal** and external routers Page 5. ...

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[CITATION] Building Your **Firewall** Rulebase

L Spitzner - Lance's Security Papers. January, 2000

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Key management for network communication

RH Levesque, JA Kraemer, AP Nadkarni - US Patent 5,825,891, 1998 - Google Patents

... A *u * ji ** * * ie 11 * 1=0 keys to other computers, sends the encrypted packet to **internal firewall** computer 158 in accordance with the tunnel database. ...

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Corporate system security: towards an integrated management approach- ► [bubl.ac.uk](#)

HN Higgins - Information Management & Computer Security, 1999 - [emeraldinsight.com](#)

... Administrators should manage users' privileges to prevent **internal** users from accessing ...
limit the chance of users who sneak in through holes in the **firewall**. ...

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Transferring encrypted packets over a public network

AJ Kirby, JA Kraemer, AP Nadkarni - US Patent 5,898,784, 1999 - Google Patents
... 5,898,784 5 6 If the header 82 is not a swIPe header, then the security sends the
encrypted packet to **internal firewall** computer 158 network driver issues a ...

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[PDF] ► Management of network security applications

PC Hyland, R Sandhu - Proceedings of the 21st NIST-NCSC National Information ..., 1998 - csrc.nist.gov
... **Internal Firewall** External **Firewall** Organizational Boundary NMS External **Firewall** ...
SNMP Agent Figure 3. Management of **Internal** vs. External **Firewalls** Page 10. ...

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X through the **firewall**, and other application relays

GW Treese, A Wolman - Proceedings of the USENIX Summer 1993 Technical Conference ..., 1993 -
portal.acm.org

... Screening Router External Network **Internal** Network **Internal** Hosts Other **Internal**
Networks X" s \$k@de}873qD ~ y Xug \$X" k@d@" q DhjisfIC s \$k (l isf Page 5. ...

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